Rylan Wong Gupta

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WORK EXPERIENCE

Research Intern — **Albany, NY**

International Business Machines (IBM) — June 2020 – August 2020

- Research intern within IBM's Artificial Intelligence (AI) Hardware Research Center for RPI's AiMOS AI Supercomputer.
- Contributed to development of Proof of Concept (PoC) computer systems to design, test, and implement containerization technologies and composable infrastructure in High Performance Compute (HPC) applications for AI.
- Participated in detailed engineering and design discussions and processes for projects to apply them to AI HPC.
- Wrote extensive documentation on the setup and utilization of cutting edge PoC systems.
- Technologies: HPC, Red Hat OpenShift and Open Data Hub, Linux, Docker, PCIe switching, composable infrastructure

Technical Consulting Engineer Co-op — **Research Triangle Park, NC**

Cisco Systems, Inc. — January 2019 – June 2019

- Solved over 60 advanced customer Virtual Private Network issues, such as Internet Protocol Security tunnels on Cisco's Security and Networking platforms as part of Cisco's highest escalation Backbone VPN Technical Assistance Center team.
- Set up network topologies with Cisco's entire portfolio of technology platforms in their Technical Services Lab.
- Skills: Networking, security, collaboration, troubleshooting, VPN, customer technical support, workflow automation

Software Development Engineer — **Princeton, NJ**

FansRaise — June 2017 – September 2017

- Designed and engineered a full and extensive end-to-end testing suite for web-based fundraising platform, implemented and written using JavaScript to test full stack functionality and UX, facilitating over $338,000 raised for over 100 musical groups.
- Skills: Automated E2E testing, Git, Black-box testing, project and small team communication, Node.js, Nightwatch.js

EDUCATION

Rensselaer Polytechnic Institute (RPI) — **Troy, NY**

B.S. Computer Systems Engineering, minor in Computer Science   GPA: 3.35   August 2017 – December 2020

- Relevant Coursework:

Operating Systems	Embedded Control	Algorithms	Programming Languages	Electronics
Engineering Design	Data Structures	Hardware Design	Multidisciplinary Capstone	Circuits
VLSI Design	Signals and Systems	Computer Architecture	Differential Equations	Chinese III & IV

- Technical Skills: Java (4yr), Git (4yr), Node.js (3yr), Linux(2yr), C++ (1y), C (1.5yr), MIPS assembly (1yr), Python (1yr)

AWARDS

- 1st Place Project at HackRPI 2019 – Natural Disaster and Mitigation
- FCA CES 2020 Showcase Design Competition Winner
- Rensselaer Leadership Award

LEADERSHIP

- OpenCircuits.io Project Co-Leader (1yr)
- Drum Corps Baritone Section Leader (3yr), Horn Sargent (1yr)
- Jazz Trombone Section Leader (2yr)

PROJECTS & ACTIVITIES

Multidisciplinary Capstone Design Project: Growth Chamber Sensor System — **Troy, NY**

Cloud and Docker Specialist — August 2020 – Current

- The Growth Chamber Sensor System detects errors within a growth chamber and displays monitored data to research scientists within RPI's Lighting Enabled Systems & Applications (LESA) Center.
- Currently designing backend services stack to support data transmission, storage, and GUI applications.

OpenCircuits.io and OpenCircuits EE — **Troy, NY**

Project Co-Leader and Contributor — August 2018 – December 2019

- Open source, web-based, free, and easy to use circuit designer for building digital logic circuits .
- Project Manager researching analog circuit analysis algorithms and libraries, and added Docker support to the project.

CERTIFICATIONS

Cisco Certified Network Associate - Routing & Switching (CCNA R&S) — **Cisco Systems, Inc.**
Cisco Certified Network Associate - Security (CCNA Security)   Cisco ID: CSCO13433883   June 2019 – June 2022